



SANMARG SHIKSHAN SANSTHA NAGPUR

UNDER THE SOCIETIES REGISTRATION ACT XXI OF 1860 R. NO. 40/66 M.S. & Charitable Act. Reg.(814) (N)

MADHUKARRAO PANDAV COLLEGE OF ENGINEERING

Bhilewada, Bhandara. Ph. 07184-309400/402/403/411

Website: www.mpce.org.in email: mpce@rediffmail.com, tnp.mpce@rediffmail.com

Department of Computer Engineering

SESSION: 2023-24

Notice

Date:- 01/10/2024

Dear Students, I would like to inform you that an "Objective Test" of 50 marks For all subjects is scheduled on 05/10/2024 from 2:30pm to 3:30pm. The test will cover Multiple Choice Questions (MCQs) from the first and second unit of each subject.


H. Q. D.
Department of Computer Engineering
Madhukarrao Pandav College of
COMPUTER ENGINEERING


PRINCIPAL
Madhukarrao Pandav College of
Engineering, Bhilewada,
Bhandara.



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Department of Computer Engineering

SESSION: 2023-24

Exam Title: IQ for slow Learner and Advances Learner MCQ Exam

Total Marks : 50

Instructions:

Answer all questions.

Each question carries one marks.

Choose the most appropriate option for each question.

There is no negative Marking.

1. What is Digital Electronics? a) Field of electronics involving the study of digital signal b) Engineering of devices that digital signal c) Engineering of devices that produce digital signal d) All pf the mentioned	2. Which of the following is correct for Digital Circuits? a) Less susceptible to noise or degradation in quality b) Use transistors to create logic gates to perform Boolean logic c) Easier to perform error detection and correction with digital signal d) All of the mentioned
3. What is a Circuit? a) Open-loop through which electrons can pass b) Closed-loop through which electrons can pass c) Closed-loop through which Neutrons can pass d) None of the mentioned	4. Which of the following is an example of a digital Electronic? a) Computers b) Information appliances c) Digital cameras d) All of the mentioned
5. Which of the following is a type of digital logic circuit? a) Combinational logic circuits b) Sequential logic circuits c) Both Combinational & Sequential logic circuits d) None of the mentioned	6. Which of the following options comes under the non - saturated logic family in Digital Electronics? a) Emitter - coupled Logic b) High-Threshold Logic c) Integrated - injection Logic d) Diode - Transistor Logic
7. What is a switching function that has more than one output called in Digital Electronics? a) Multi-gate function b) Multi-output function c) Multiple-gate function d) Multiple-output function	8. Which characteristic of IC in Digital Circuits represents a function of the switching time of a particular transistor? a) Fan - out b) Fan - in c) Power dissipation d) Propagation delay

<p>9. When can one logic gate drive many other logic gates in Digital Electronics?</p> <p>a) When its output impedance is low and the input impedance is low</p> <p>b) When its output impedance is high and the input impedance is high</p> <p>c) When its output impedance is high and the input impedance is low</p> <p>d) When its output impedance is low and the input impedance is high</p>	<p>10. Which of the following digital logic circuits can be used to add more than 1 – bit simultaneously?</p> <p>a) Full – adder</p> <p>b) Ripple – carry adder</p> <p>c) Half – adder</p> <p>d) Serial adder</p>
<p>11. When does a negative level triggered flip-flop in Digital Electronics changes its state?</p> <p>a) When the clock is negative</p> <p>b) When the clock is positive</p> <p>c) When the inputs are all zero</p> <p>d) When the inputs are all one</p>	<p>12. . What is a computer network?</p> <p>a) A device used to display information on a computer screen</p> <p>b) A collection of interconnected computers and devices that can communicate and share resources</p> <p>c) A type of software used to create documents and presentations</p> <p>d) The physical casing that protects a computer's internal components</p>
<p>13. What is internet?</p> <p>a) A network of interconnected local area networks</p> <p>b) A collection of unrelated computers</p> <p>c) Interconnection of wide area networks</p> <p>d) A single network</p>	<p>14. Which of the following is an example of Bluetooth?</p> <p>a) wide area network</p> <p>b) virtual private network</p> <p>c) local area network</p> <p>d) personal area network</p>
<p>15. What is the full form of OSI?</p> <p>a) optical service implementation</p> <p>b) open service Internet</p> <p>c) open system interconnection</p> <p>d) operating system interface</p>	<p>16. When a collection of various computers appears as a single coherent system to its clients, what is this called?</p> <p>a) mail system</p> <p>b) networking system</p> <p>c) computer network</p> <p>d) distributed system</p>
<p>17. How many layers are there in the ISO OSI reference model?</p> <p>a) 7</p> <p>b) 5</p> <p>c) 4</p> <p>d) 6</p>	<p>18. What are nodes in a computer network?</p> <p>a) the computer that routes the data</p> <p>b) the computer that terminates the data</p> <p>c) the computer that originates the data</p> <p>d) all of the mentioned</p>
<p>19. Which one of the following is not a function of network layer?</p> <p>a) congestion control</p> <p>b) error control</p> <p>c) routing</p> <p>d) inter-networking</p>	<p>20. Which of the following devices forwards packets between networks by processing the routing information included in the packet?</p> <p>a) firewall</p> <p>b) bridge</p> <p>c) hub</p> <p>d) router</p>

<p>21. Which layer does the data link layer take packets from and encapsulate them into frames for transmission?</p> <p>a) transport layer b) application layer c) network layer d) physical layer</p>	<p>22. State true or false? Statement: An NFA can be modified to allow transition without input alphabets, along with one or more transitions on input symbols.</p> <p>a) True b) False</p>
<p>23. Statement 1: ϵ- transition can be called as hidden non-determinism. Statement 2: $\delta(q, \epsilon) = p$ means from q it can jump to p with a shift in read head. Which among the following options is correct?</p> <p>a) Statement 1 and 2, both are correct b) Statement 1 and 2, both are wrong c) Statement 1 is correct while Statement 2 is wrong d) Statement 1 is wrong while Statement 2 is correct</p>	<p>24. ϵ- closure of q1 in the given transition graph:</p> <p>a) {q1} b) {q0, q2} c) {q1, q2} d) {q0, q1, q2}</p>
<p>25. Predict the total number of final states after removing the ϵ-moves from the given NFA?</p> <p>a) 1 b) 2 c) 3 d) 0</p>	<p>26. For NFA with ϵ-moves, which among the following is correct?</p> <p>a) $\Delta: Q \times (\Sigma \cup \{\epsilon\}) \rightarrow P(Q)$ b) $\Delta: Q \times (\Sigma) \rightarrow P(Q)$ c) $\Delta: Q \times (\Sigma^*) \rightarrow P(Q)$ d) All of the mentioned</p>
<p>27. Which among the following is false? ϵ-closure of a subset S of Q is:</p> <p>a) Every element of S \in Q b) For any $q \in \epsilon(S)$, every element of $\delta(q, \epsilon)$ is in $\epsilon(S)$ c) No other element is in $\epsilon(S)$ d) None of the mentioned</p>	<p>28. The automaton which allows transformation to a new state without consuming any input symbols:</p> <p>a) NFA b) DFA c) NFA-I d) All of the mentioned</p>
<p>29. ϵ-transitions are</p> <p>a) conditional b) unconditional c) input dependent d) none of the mentioned</p>	<p>30. The _____ of a set of states, P, of an NFA is defined as the set of states reachable from any state in P following ϵ-transitions.</p> <p>a) ϵ-closure b) ϵ-pack c) Q in the tuple d) None of the mentioned</p>
<p>31. Which is the correct syntax of inheritance?</p> <p>a) <code>class base_classname : access derived_classname { /*define class body*/ };</code> b) <code>class derived_classname : access base_classname { /*define class body*/ };</code> c) <code>class derived_classname : base_classname { /*define class body*/ };</code> d) <code>class base_classname : derived_classname { /*define class body*/ };</code></p>	<p>32. The feature by which one object can interact with another object is _____</p> <p>a) Message reading b) Message Passing c) Data transfer d) Data Binding</p>

<p>33. Which among the following, for a pure OOP language, is true?</p> <p>a) The language should follow at least 1 feature of OOP</p> <p>b) The language must follow only 3 features of OOP</p> <p>c) The language must follow all the rules of OOP</p> <p>d) The language should follow 3 or more features of OOP</p>	<p>34. What is an abstraction in object-oriented programming?</p> <p>a) Hiding the implementation and showing only the features</p> <p>b) Hiding the important data</p> <p>c) Hiding the implementation</p> <p>d) Showing the important data</p>
<p>35. In which access should a constructor be defined, so that object of the class can be created in any function?</p> <p>a) Any access specifier will work</p> <p>b) Private</p> <p>c) Public</p> <p>d) Protected</p>	<p>36. Which among the following represents correct constructor?</p> <p>a) <code>-classname()</code></p> <p>b) <code>classname()</code></p> <p>c) <code>()classname</code></p> <p>d) <code>~classname()</code></p>
<p>37. How to access data members of a class?</p> <p>a) Dot, arrow or direct call</p> <p>b) Dot operator</p> <p>c) Arrow operator</p> <p>d) Dot or arrow as required</p>	<p>38. Where is the memory allocated for the objects?</p> <p>a) Cache</p> <p>b) ROM</p> <p>c) HDD</p> <p>d) RAM</p>
<p>39. Which feature of OOP is exhibited by the function overriding?</p> <p>a) Polymorphism</p> <p>b) Encapsulation</p> <p>c) Abstraction</p> <p>d) Inheritance</p>	<p>40. Which feature can be implemented using encapsulation?</p> <p>a) Polymorphism</p> <p>b) Overloading</p> <p>c) Inheritance</p> <p>d) Abstraction</p>
<p>41. Which are the Fourier coefficients in the following?</p> <p>(a) a_0, and b_n</p> <p>(b) a_n</p> <p>(c) b_n</p> <p>(d) and b_n</p>	<p>42. Do exponential Fourier series also have Fourier coefficients to be evaluated.</p> <p>(a) True</p> <p>(b) False</p>
<p>43. Fourier series uses which domain representation of signals?</p> <p>(a) Time domain representation</p> <p>(b) Frequency domain representation</p> <p>(c) Both combined</p> <p>(d) Neither depends on the situation</p>	<p>44. "A periodic function" is given by a function which</p> <p>(a) Has a period $T = 2\pi$;</p> <p>(b) Satisfied $(t +) = f(t)$;</p> <p>(c) Satisfied $(+ T) = - f(t)$;</p> <p>(d) Has a period $T = \pi$.</p>

<p>45. The right-hand limit of (x) at t is represented by.....</p> <p>(a) $\lim_{\epsilon \rightarrow 0(t+\epsilon)}$, where ϵ is positive</p> <p>(b) $(t+)$</p> <p>(c) $(t+0)$</p> <p>d) All of the above</p>	<p>46. A graph of periodic function (x) that has period L exhibits the same pattern every L units along the x-axis.</p> <p>(a) True</p> <p>(b) False</p>
<p>47. Find the inverse Laplace transform for $1/s$.</p> <p>(a) 1</p> <p>(b) t</p> <p>(c) eat</p> <p>(d) -1</p>	<p>48. The Laplace transform of a null function (t) is -----</p> <p>(a). 0.</p> <p>(b) 1.</p> <p>(c). (a) & (b)</p> <p>(d). None of the above</p>
<p>49. The inverse Laplace transformation operator is linear.</p> <p>(a) True</p> <p>(b) False</p>	<p>50. What are Fourier coefficients?</p> <p>(a) The terms that are present in a Fourier series</p> <p>(b) The terms that are obtained through Fourier series</p> <p>(c) The terms which consist of the Fourier series along with their sine or cosine values</p> <p>d) None of the above</p>

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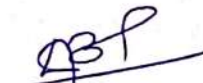
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Engineering, Bhilewada, Bhandara.

Answer Key:-

Q. No.	Option	Q. No.	Option
1	d	26	a
2	d	27	d
3	b	28	a
4	d	29	b
5	c	30	a
6	a	31	b
7	b	32	b
8	d	33	c
9	d	34	a
10	b	35	c
11	a	36	b
12	b	37	d
13	c	38	d
14	d	39	a
15	c	40	d
16	d	41	a
17	a	42	a
18	d	43	b
19	b	44	b
20	d	45	d
21	c	46	a
22	a	47	a
23	c	48	a
24	c	49	a
25	c	50	c


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MADHUKARRAO PANDAV COLLEGE OF ENGINEERING BHILEWADA, BHANDARA

DEPARTMENT OF COMPUTER ENGINEERING

SESSION :- 2023-24 (ODD SEM)

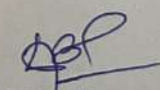
SEMESTER :- THIRD SEM (B.TECH) (COMPUTER ENGINEERING)

CALCULATION REPORT FOR ADVANCE & SLOW LEARNER LIST

SR.NO	NAME OF STUDENTS	MCQ (50 Marks)	Total Marks/Percentage (100%)	ADVANCE LEARNER /SLOW LEARNER
1	MUNGMODE LEENA ASHOK	40	80.00	Advance Learner
2	DHANDE MONALEE GYANIRAM	42	84.00	Advance Learner
3	NEWARE PAYAL HIRALAL	41	82.00	Advance Learner
4	SHILAR RESHMA RAVINDRA	30	60.00	Advance Learner
5	MESHARAM RUTUJA RAJKUMAR	33	66.00	Advance Learner
6	DESHMUKH SAMIKSHA SUNIL	31	62.00	Advance Learner
7	TALOKAR SANSKRUTI M.	40	80.00	Advance Learner
8	MEHAR SEJAL CHATUR	38	76.00	Advance Learner
9	TAMBE SHARAYU SHEKHAR	27	54.00	Slow Learner
10	BORKAR SHRAWANI RAJU	36	72.00	Advance Learner
11	RASEKAR SHRUTI DINESH	33	66.00	Advance Learner
12	RAHANGDALE SHWETA RUPESH	32	64.00	Advance Learner
13	KATHANE VAISHNAVI DEORAM	30	60.00	Advance Learner
14	MESHARAM VAISHNAVI MAHESH	40	80.00	Advance Learner
15	ARBAZ AHAMAD IRFAN AHAMAD	41	82.00	Advance Learner
16	BANTE ARPIT VIJAY	45	90.00	Advance Learner
17	WANVE ASHWIN SANDIP	42	84.00	Advance Learner
18	MANDAWKAR ATHARV MADHAV	41	82.00	Advance Learner
19	NAGPURE CHAMAN YOGRAJ	33	66.00	Advance Learner
20	CHANDEWAR GIRIDHAR VILAS	38	76.00	Advance Learner
21	KHOTELE HRUTIK PUSHPAK	30	60.00	Advance Learner
22	PINGALE KARTIK UDARAM	38	76.00	Advance Learner
23	KADGAYE KRUNAL MUNESHWAR	31	62.00	Advance Learner
24	SHENDE PIYUSH ARUN	36	72.00	Advance Learner
25	HAGDE RESHAM SUNIL	37	74.00	Advance Learner

26	SHENDE RITIK SUBHASH	34	68.00	Advance Learner
27	CHACHANE TULANYA P.	33	66.00	Advance Learner
28	HALMARE UJWAL SHIVSHANKAR	31	62.00	Advance Learner
29	SATHONE VAIBHAV GURUDAS	34	68.00	Advance Learner
30	BAGHELE SHUBHAM SHIVLAL	26	52.00	Slow Learner
31	BISANE JYOTSNA SHRICHAND	41	82.00	Advance Learner
32	BORKAR MANISH SANJAY	41	82.00	Advance Learner
33	CHOUDHARI KHUSHBU B.	42	84.00	Advance Learner
34	DHOMANE ARYA SANJIVKUMAR	30	60.00	Advance Learner
35	GAJAPURE ABOLI OMPRAKASH	38	76.00	Advance Learner
36	GAYDHANE SAGAR LEKHIRAM	39	78.00	Advance Learner
37	JAMBHULKAR ASHWINI SOMA	37	74.00	Advance Learner
38	JAMBHULKAR BHAGYASHREE V.	30	60.00	Advance Learner
39	KAMANE PIYUSH AMRUT	32	64.00	Advance Learner
40	KAUSTUBH KATRE	42	84.00	Advance Learner
41	KUNJEKAR AYUSH VIJAY	40	80.00	Advance Learner
42	MANDADE NASHIK BABU	40	80.00	Advance Learner
43	MASURKAR PRACHI DILIP	30	60.00	Advance Learner
44	MULE ADITYA VENUMADHAV	38	76.00	Advance Learner
45	NEWARE NEHA SHANKAR	38	76.00	Advance Learner
46	NIKHARE LUSHANT DHANRAJ	31	62.00	Advance Learner
47	PARDHI BHAVESH YAMESHWAR	35	70.00	Advance Learner
48	RAHANGDALE AKSHAYKUMAR K.	35	70.00	Advance Learner
49	SHARNAGAT ACHAL SUNIL	28	56.00	Slow Learner
50	SHARNAGATE TANU SURESH	36	72.00	Advance Learner
51	SHENDE SANGHDIP RANGANLAL	42	84.00	Advance Learner
52	SONWANE UNNATI LAXMICHAND	41	82.00	Advance Learner
53	THAKRE JAYANT RAMESHWAR	40	80.00	Advance Learner
54	TULSIKUMAR C. BISANE	25	50.00	Slow Learner


 Principal
 Madhukarrao Pandav College
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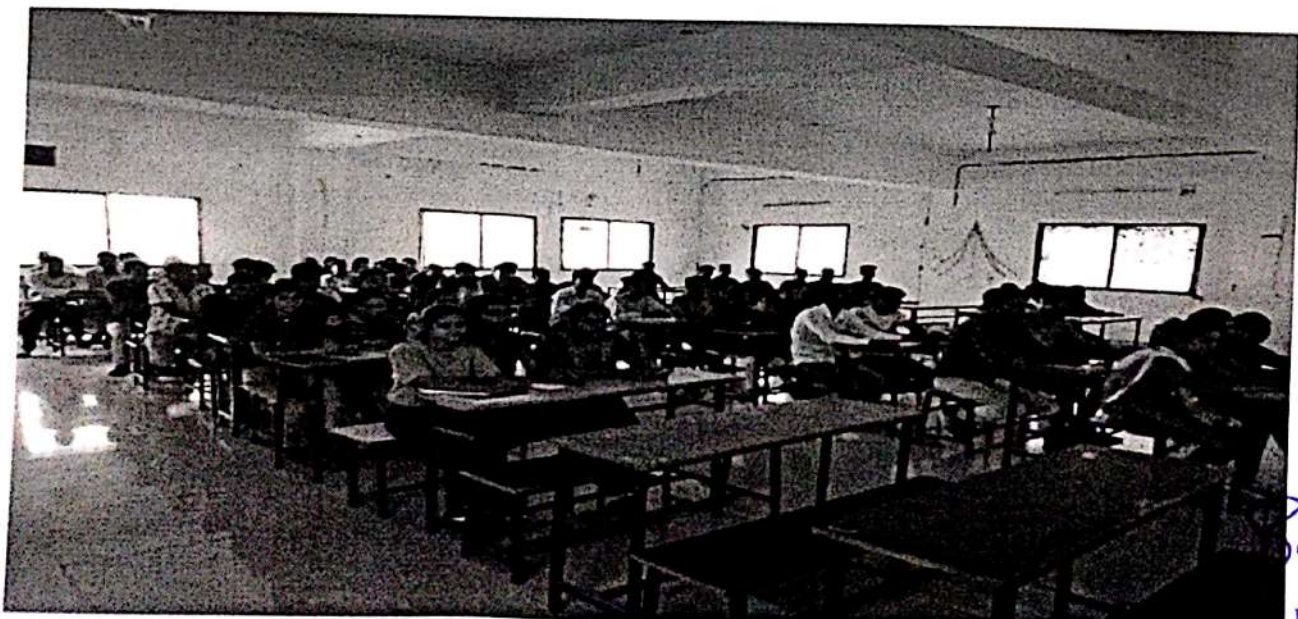
Activities for Advanced learner (Sample Copy)

Department of Computer Engineering conducted Extra lectures on Object Oriented Programming for Slow learners. (session 2023-2024)



Activities for Advanced learner (Sample Copy)

Department Computer Engineering organised workshop on Computer Network for advance learners. (session 2023-2024)





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Approved by AICTE, DTE, Govt. of Maharashtra, Affiliated to RTM Nagpur University, Nagpur

ONE PAGE REPORT ON	
Title	SDP
Name of Activity	One Day SDP on "AI in Cyber-Security"
Date	9-Feb-2024
Venue	Seminar hall
Organized by	Computer Engineering Department, MPCOE, Bhandara
Name of Expert	Mr. Jayant Rajurkar MIET, Bhandara
Participated by	Students of Computer Engineering Department, MPCOE
Activity In-charge	Mrs. Neha Waghale MPCOE, Bhandara
Content	<ol style="list-style-type: none">1. Introduction To Cyber Security2. Understanding Cyber Threat3. AI's Predictive Capabilities4. Automating Security Response5. Enhancing Threat Detection6. AI in Vulnerability Management
Objective	<ol style="list-style-type: none">1. To understand the impact of AI in future.2. To understands the importance of AI in cyber security.
Outcome of Activity	<ol style="list-style-type: none">1. Students came to know the importance of AI in cyber security .2. Students understands the various cyber threat.

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Pictures : One Day SDP on " AI in Cyber-Security "




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Approved by AICTE, DTE, Govt. of Maharashtra, Affiliated to RTM Nagpur University, Nagpur

ONE PAGE REPORT ON	
Title	SDP
Name of Activity	One Day SDP on "GREEN ENERGY TECHNOLOGY"
Date	11-Sep-2023
Venue	Seminar hall
Organized by	Computer Engineering Department, MPCE, Bhandara
Name of Expert	Mr. Prasant Tarone Vainganga College of Polytechnic , Nagpur
Participated by	Students of Computer Engineering Department, MPCE
Activity In-charge	Mrs. Neha Waghale MPCOE, Bhandara
Content	<ol style="list-style-type: none">1. Introduction to Green Energy Technology2. Solar Energy Innovation3. Wind Energy Breakthrough4. Hydrogen Fuel Advances5. Conclusion: A Sustainable Future
Objective	<ol style="list-style-type: none">1. To understand the Green energy technology and its importance in future.2. To understand the technology to conserve the fuel for sustainable future.
Outcome of Activity	<ol style="list-style-type: none">1. Students came to know the green energy technology.2. Students understands the various technology for saving fuel.

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Pictures : One Day SDP on "Green Energy Technology"



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